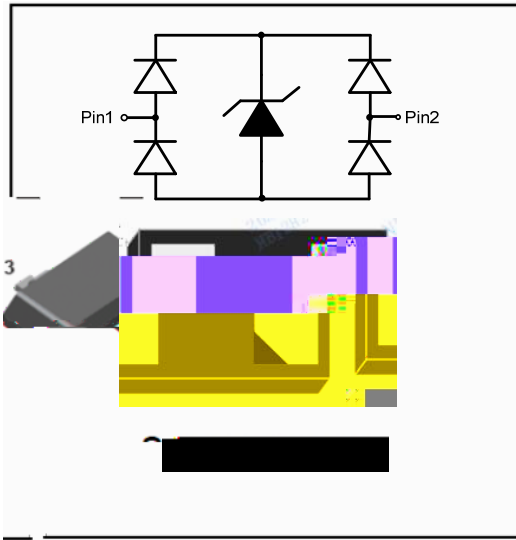


1- Line, Bi-directional, Ultra-low Capacitance Transient Voltage Suppressor



Features

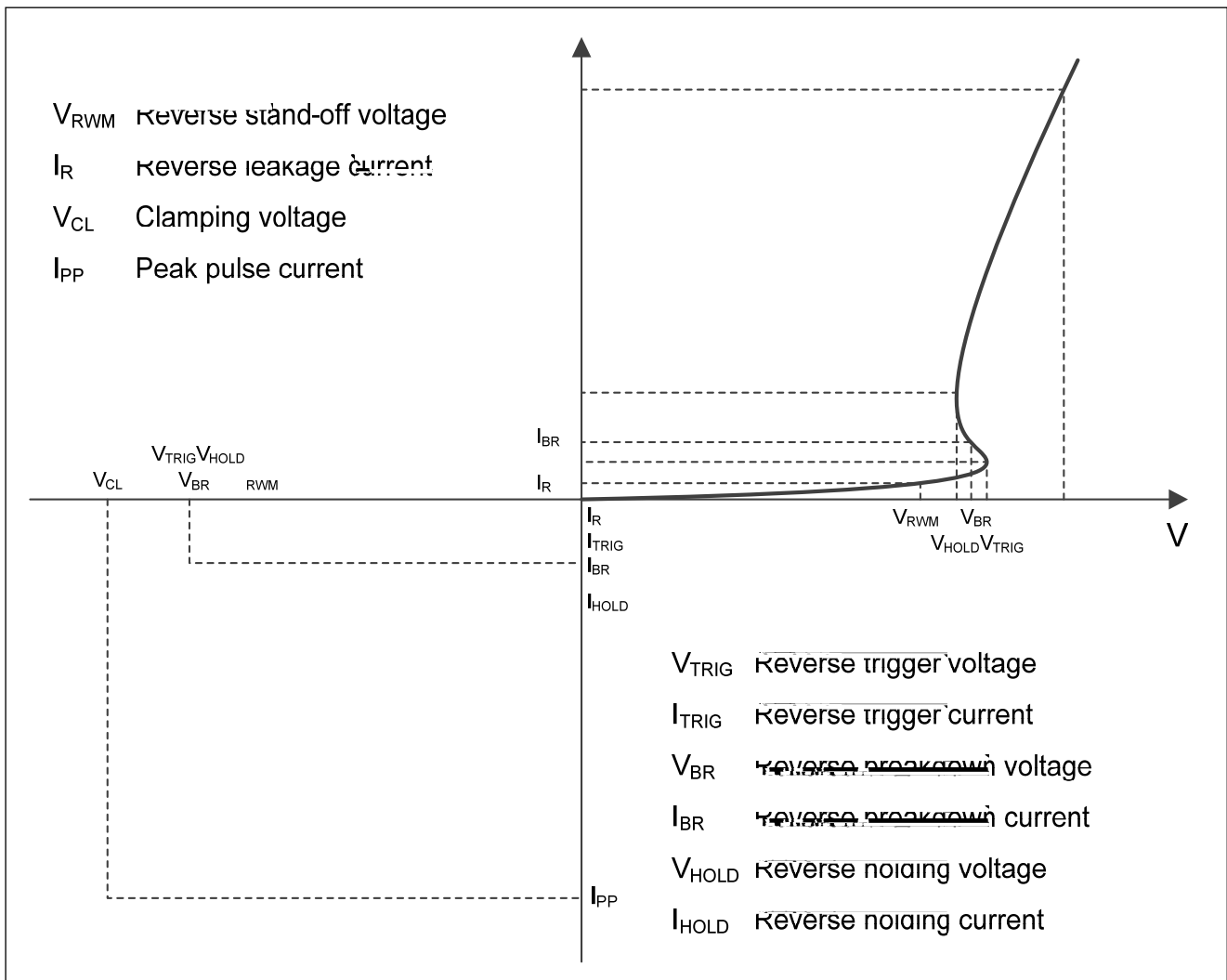
- Stand-off voltage: 5V Max
- Transient protection for each line according to IEC61000-4-2(ESD): $\pm 15\text{kV}$ (contact)
- IEC61000-4-5(surge): 4A (8/20 μs)
- Ultra-low capacitance: $C_J = 0.35\text{pF}$ typ
- Ultra-low inductance: $L_P = 0.1\text{nH}$ typ
- Package: DFN1006-2

Terminals: Tin plated leads, solderable per J-STD-002 and JESD22-B102

Polarity: Cathode line denotes the cathode end

Marking: 5U

Definitions of Electrical Characteristics







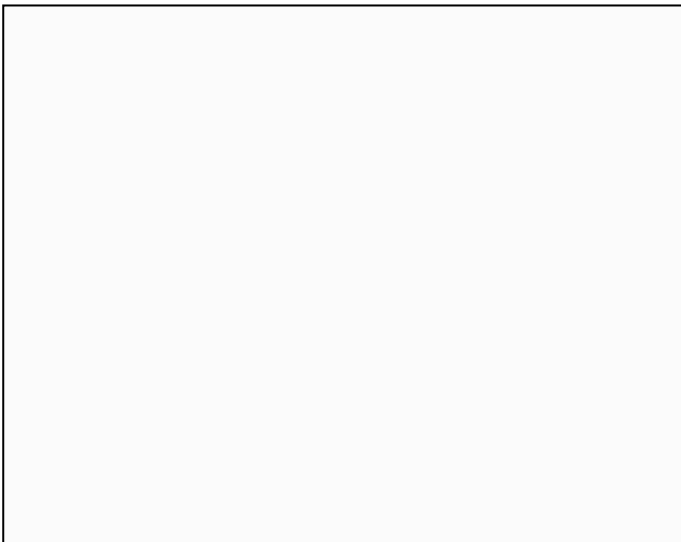
ESDSL5V0LBS

ESD damping
(+8kV contact discharge per IEC61000-4-2)

ESD damping
(-8kV contact discharge per IEC61000-4-2)

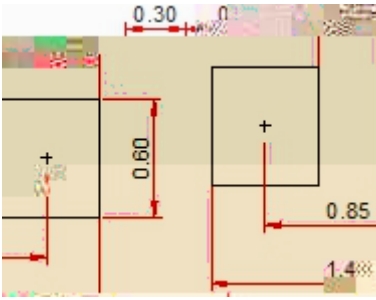
TLP Measurement

Outline Dimensions





Recommend land pattern (Unit:mm)



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met



ESDSL C5V0LBS

Disclaimer

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